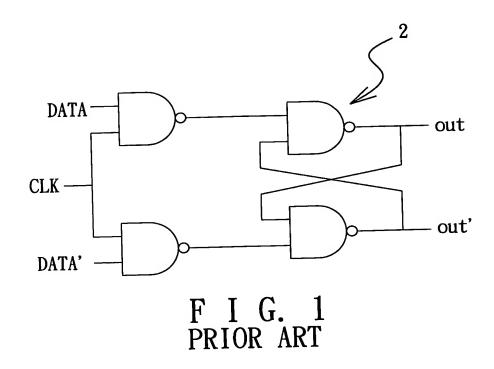
THEREFOR

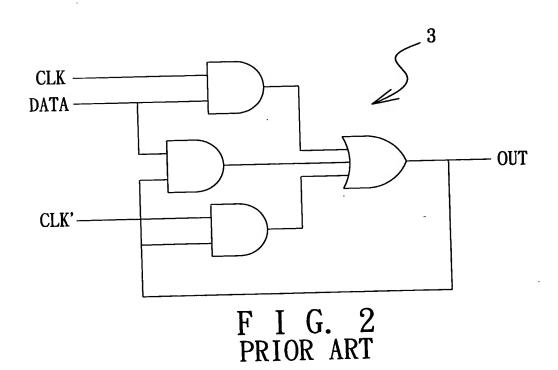
Inventors:

Tsin-Yuan Chang; Hao-Yung Lo;

and Shao-Sheng Yang

Docket No.: SIPT121661

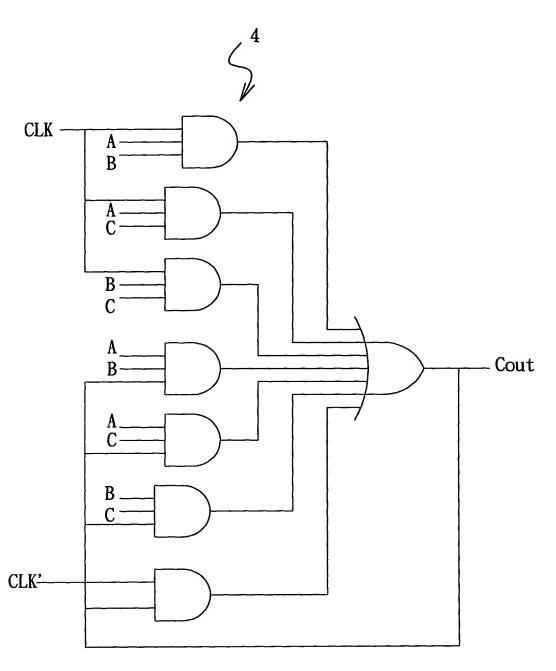




THEREFOR

Inventors:

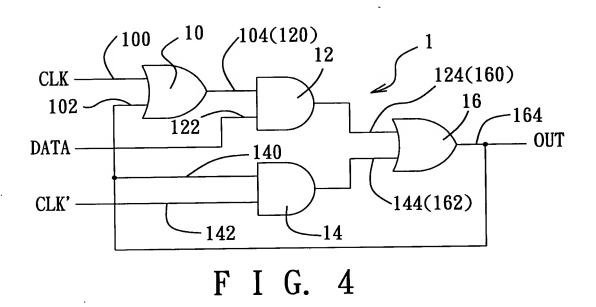
Tsin-Yuan Chang; Hao-Yung Lo;

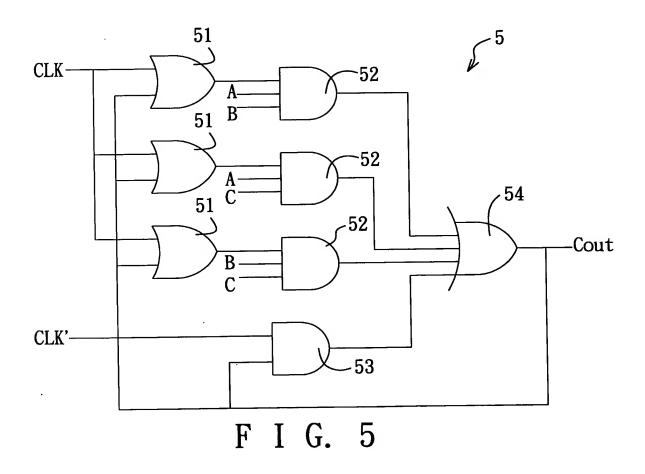


F I G. 3 PRIOR ART

THEREFOR

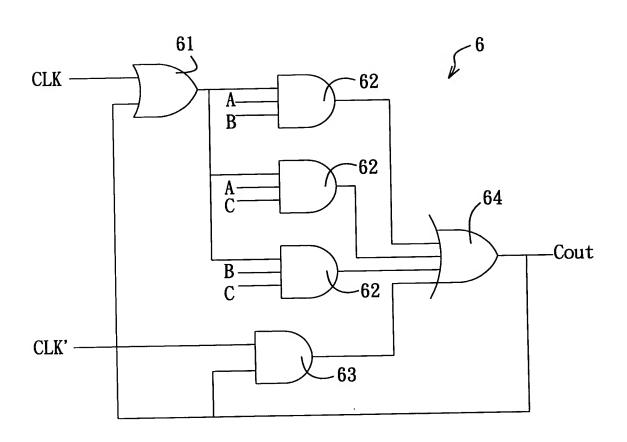
Tsin-Yuan Chang; Hao-Yung Lo; Inventors:





THEREFOR

Tsin-Yuan Chang; Hao-Yung Lo; Inventors:



F I G. 6

Tsin-Yuan Chang; Hao-Yung Lo; Inventors: and Shao-Sheng Yang
Docket No.: SIPT121661 F I G. 7 ∞ [__ [工 140n 140n 140n 120n 120n 120n 100n 100n 16 E In 80n Time (lin) (TIME) n 80n Time (lin) (TIME) Time(lin)(TIME) cout_original 40n ê cout_proposed, sum_proposed, ğ 20n 20n N (nil) segetloV Vol Vol Vol Vol (nil) sagetloV D Δ DO: AO: v(clk) DO: A0: v(co) DO: AO: v(cp) DO: AO: v(so) DO: AO: v(sp) DO: AO: v(c) 00:A0:v(b)

Mave

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THEREFOR

Title: FEEDBACK LATCH CIRCUIT AND METHOD

Mave

THEREFOR

Tsin-Yuan Chang; Hao-Yung Lo; Inventors:

		т		
Modified Svensson latch	7	39	0. 209	0.148
Tri-state Modified buffer Svensson latch latch	10	48	0.396	0. 299
transmission buffer -gate latch	10	48	0. 255	0.363
	9	36	0.262	0.328
Conventional Conventional Latch Earle Latch	18	54	0.313	0.164
Conventional D-latch	18	72	0.284	0.316
Presentinvention	14	36	0.274	0.144
	Unit area	Total area	Delay (ns)	Power (mW)